

In The Specification:

Please replace paragraph [0073] with the following amended paragraph:

Some types of FPGAs are implemented with a network of programmable logic [at] and include lookup tables (LUTs). A LUT is used to implement a user-programmed logic of the LUT inputs, and the number of inputs for a particular LUT depends upon the [chitecture] architecture.

Please replace paragraph [75] with the following amended paragraph:

In the preferred embodiment of the present invention, the programming includes standard placement of any "web applications browser" software that interacts with the hardware/firmware on the board. The FPGAs are coded using C code that is either hand produced or produced from a development/design application. As is known in the art, the programming code comprises algorithms which are transforms, such as FFTs, FIRs, IIRs, Reed-Solomon, etc., that convert the analog signal (the waveform in this case) into a binary format[. Base]at the base unit signal collection point.

Please replace paragraph [0086] with the following amended paragraph:

From the foregoing it can be seen that the camera lens control apparatus may also be partially or completely under the control of the microprocessor-driven base unit. Algorithms may be provided for the complete control of zoom, focus and iris and other camera image variables to provide for the desired position change and the rate of position change of the lens system. [for] For example, the speed of convergence of the

zoom, focus and iris functions from one position to another can be controlled to work together for the smoothest performance needed in a particular application.